IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Bin YU et al.) Group Art Unit: Unassigned
Application No.: Unassigned) Examiner: Unassigned
Filed: July 8, 2003)
Title: FLASH MEMORY DEVICE)
)

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

U.S. Patent and Trademark Office 2011 South Clark Place Customer Window, Mail Stop Patent Application Crystal Plaza Two, Lobby, Room 1B03 Arlington, Virginia 22202

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), Applicant(s) bring to the attention of the Examiner the documents listed on the attached PTO 1449. This Information Disclosure Statement is being filed before the mailing date of a first Office Action in the above-referenced application. As such, no certification or fee is required. Copies of the listed documents are attached.

Applicant(s) respectfully request(s) that the Examiner consider the listed documents and indicate that they were considered by making appropriate notations on the attached form.

If any copending application(s) is/are cited on the attached PTO 1449, the Examiner's attention is directed to the foregoing application(s) in compliance with § 2001.06(b) of the Manual of Patent Examining Procedure. By identifying the copending application(s), the assignee and/or applicant of the application(s) do not waive confidentiality of the application(s). Accordingly, the U.S. Patent and Trademark Office is requested to maintain the confidentiality of the copending application(s) under 35 U.S.C. § 122.

This submission does not represent that a search has been made and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claim in the application and Applicant(s) determine(s) that the cited document(s) do not constitute "prior art" under United States law, Applicant(s) reserve(s) the right to present to the Office the relevant facts and law regarding the appropriate status of such documents.

Applicant(s) further reserve(s) the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

Information Disclosure Statement Under 37 C.F.R. § 1.97(b) Application Serial No. Unassigned Attorney's Docket No. H1130 Page 3

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 50-1070.

Respectfully submitted,

HARRITY & SNYDER, L.L.P.

Rv

Glenn Snyder Reg. No. 41,428

26615
patent trademarkoffice

11240 Waples Mill Road Suite 300 Fairfax, Virginia 22030 (571) 432-0800

Date: July 8, 2003

INFORMATION
DISCLOSURE
CITATION

26615
PATENI TRATEMARKOFFICE

ATTORNEY'S DKT No. H1130	APPLICATION No. Unassigned
APPLICANT(S) Bin YU et al.	
FILING DATE July 8, 2003	GROUP Unassigned

	PTO-1449		July 8, 2003			Unassigned		
	. 	U.S	S. PATENT DOCUMI	ENTS				
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME		CLASS	SUBCLASS	FILI DA	
·					· · · · · · · · · · · · · · · · · · ·			
	·	FORE	IGN PATENT DOCU	MENTS		,		
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY		CLASS	SUBCLASS	Trans Yes	lation No
			uding Author, Title,					
			T-A Self-Aligned Do n Devices, Vol. 47,					
	Yang-Kyu Choi 421-424.	et al., "Sub-	20nm CMOS FinFET	Technologie	s," 200	1 IEEE, IEDN	l, page	es
			0 nm P-Channel Fin y 2001, pages 880-		ransacti	ons on Electi	on	
	Xuejue Huang e	et al., "Sub 5	0-nm FinFET: PMO	S," 1999 IEE	E, IEDM	1, pages 67-7	'O.	
			scale CMOS Spacer 1, January 2002, p		he Teral	bit Era," IEEE	Electi	ron
EXAMINER	DATE CONSIDERED							

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).